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Date : June 23, 2005

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Re: Our Docket No. 55855-DIV (71987)
U.S. Serial No. 10/719,912

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PAGE 1/8 * RCVD AT 6/23/2005 5:07:38 PM [Eastern Daylight Time] * SVR:USPTO-EFXRF-1/3 * DNIS:8729306 * CSID:617 439 4170 * DURATION (mm:ss):02:36

Docket No. 55855-DIV (71987)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: T. Ho et al.

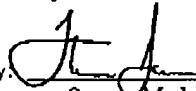
U.S. SERIAL NO.: 10/719,912 GROUP: 2822

FILED: November 21, 2003 EXAMINER: K. Duong

FOR: METHOD OF FABRICATING A THERMALLY ENHANCED WAFER-LEVEL CHIP SCALE PACKAGE

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By 
Steven M. Jensen

Commissioner for Patents
P.O. Box 1450
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Sir/Madam:

AMENDMENT

Applicants are in receipt of the Office Action dated March 23, 2005 of the above-referenced application. Please amend the application as follows:

Amendments to the specification begin on page 2 of this paper.

Amendments to the claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks begin on page 5 of this paper.